



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of :  
DiStefano, et al. : Group Art Unit: 2812  
: :  
U.S. Application No. 09/638,079 :  
: Examiner:  
Filed: August 14, 2000 :  
: :  
For: METHODS FOR PROVIDING : Date: November 17, 2000  
VOID-FREE LAYERS FOR :  
SEMICONDUCTOR ASSEMBLIES :  
: X

Assistant Commissioner For Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is of record in Application No. 08/610,610, the benefit of which is claimed under §120. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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11/20/00, 1:00 PM

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on November 17, 2000.

*Kimberly V. Perry*  
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Typed or Printed Name of Person Signing Certificate

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